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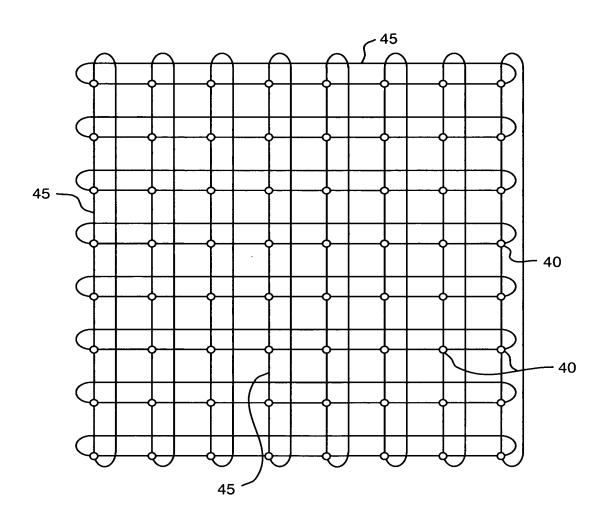


FIG. 1

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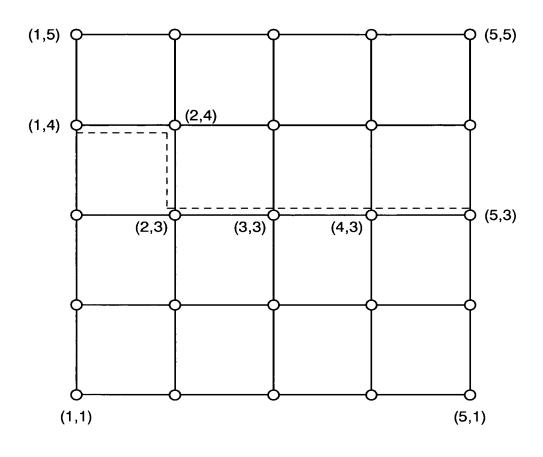
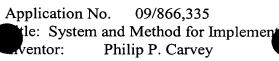


FIG. 2

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The first state of the state of



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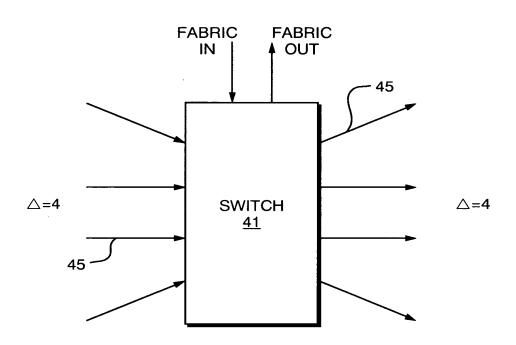


FIG. 3

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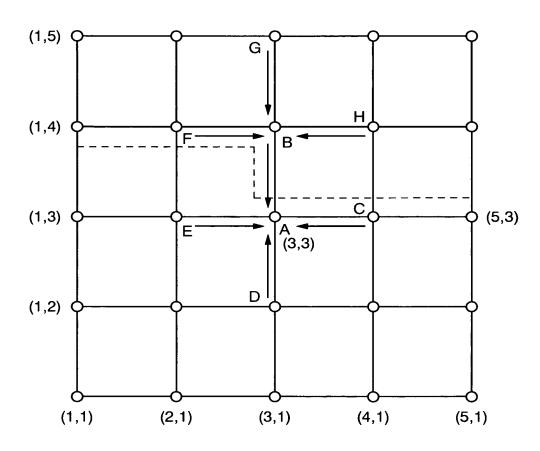
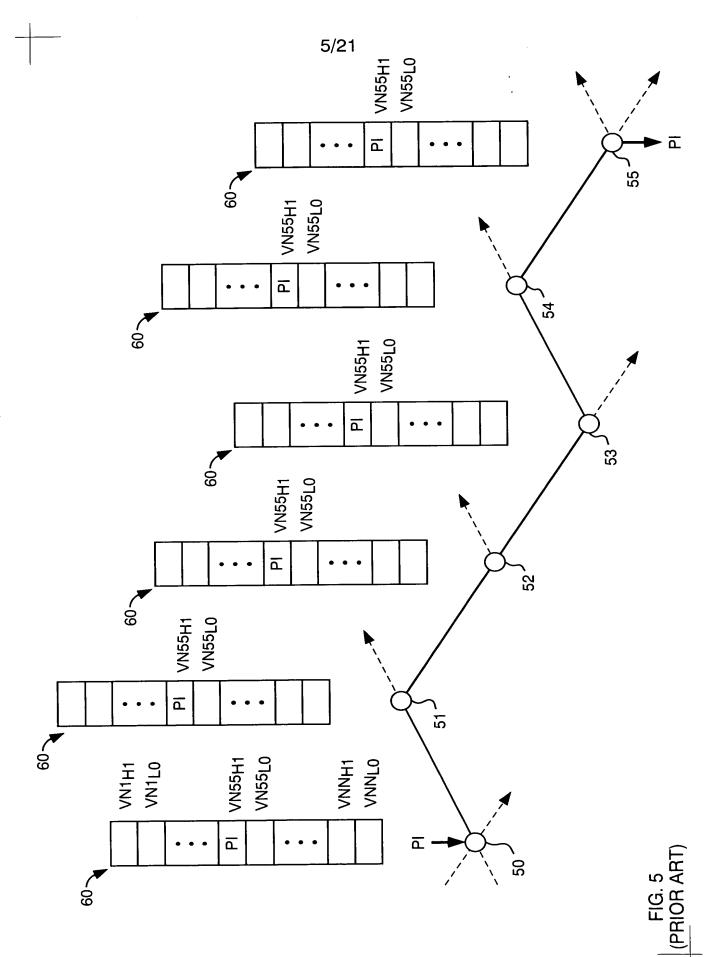


FIG. 4

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Sitle: System and Method for Impleme g...

hventor: Philip P. Carvey



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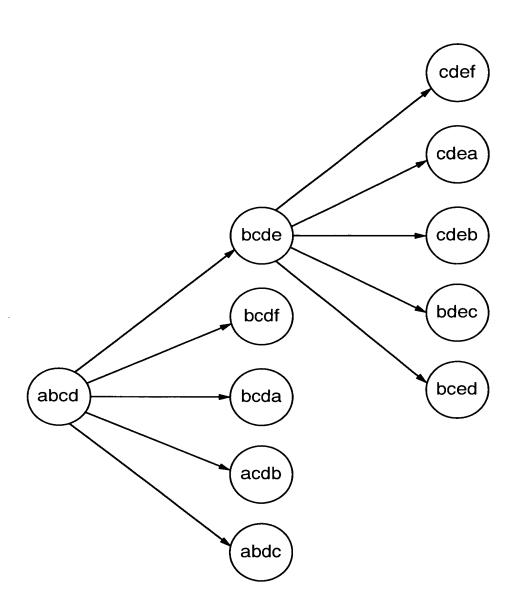


FIG. 6A

The first state of the state of

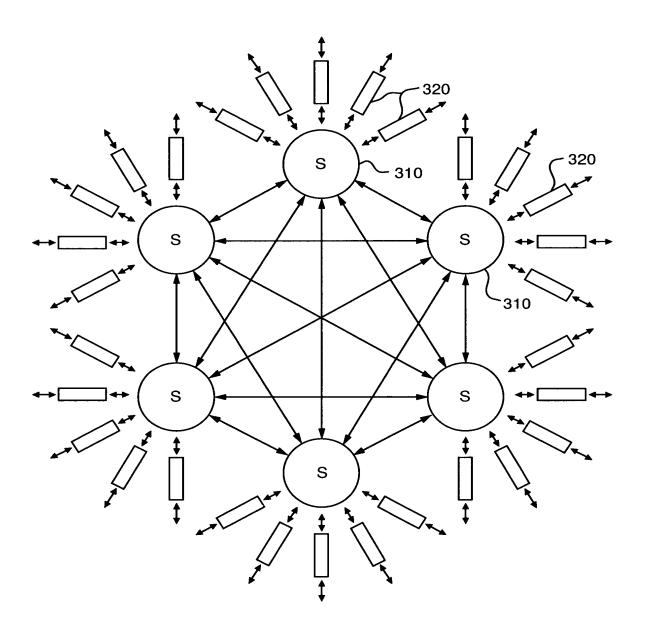
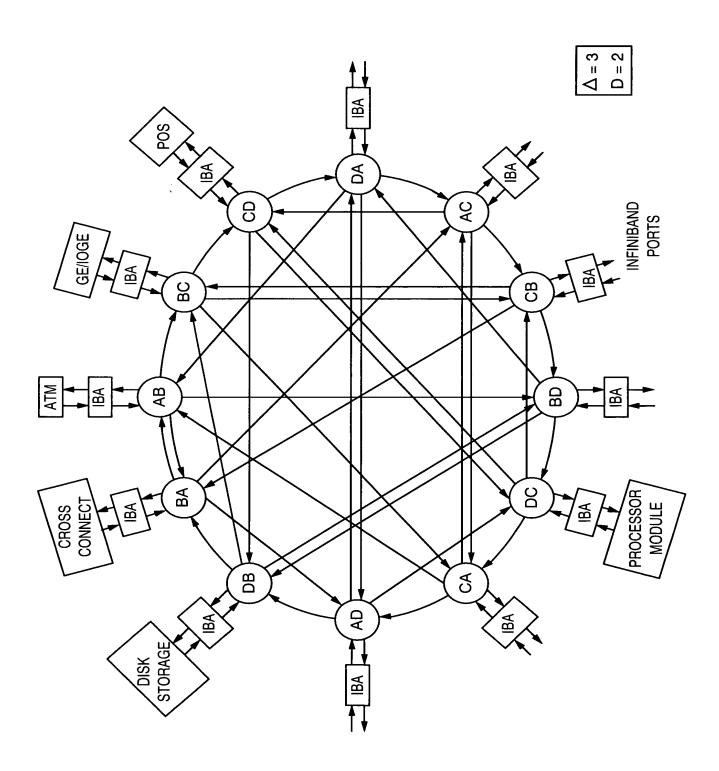


FIG. 6B

oplication No. 09/866,335 le: System and Method for Implement ventor: Philip P. Carvey





Application No. 09/866,335
tle: System and Method for Implemer ...
Philip P. Carvey

AB AB → BC AB → BD AB → BA	AC AC - CB AC - CD AC - CA	AD - DB AD - DC AD - DA
BA BA → AC BA → AD BA → AB	BC BC → CD BC → CA BC → CB	BD BD → DA BD → DC BD → DB
CA CA → AB CA → AD CA → AC	CB CB → BD CB → BA CB → BC	CD - DA CD - DB CD - DC
DA DA → AB DA → AC DA → AD	DB DB → BA DB → BC DB → BD	DC DC - CA DC - CB DC - CD

FIG. 7B

oplication No. 09/866,335 tle: System and Method for Implement ventor: Philip P. Carvey ventor:

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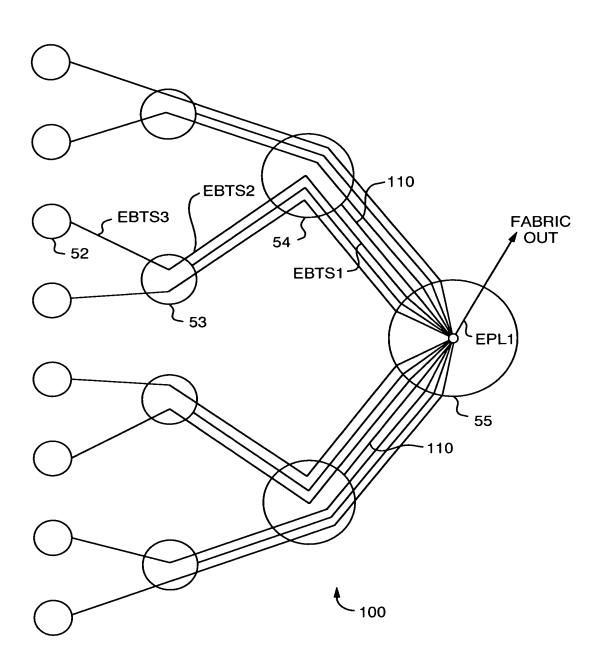


FIG. 8A



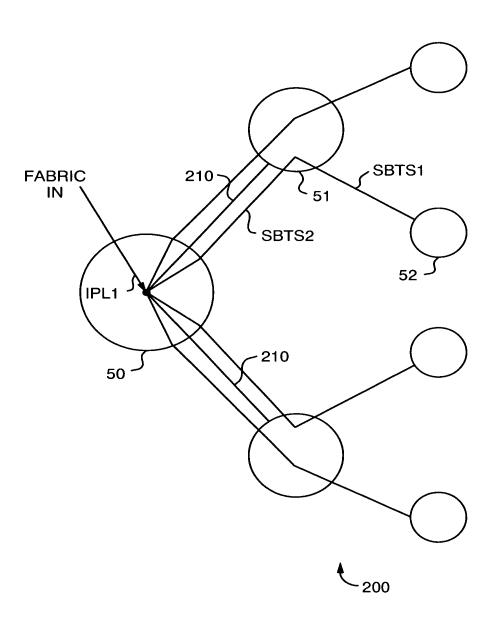


FIG. 8B

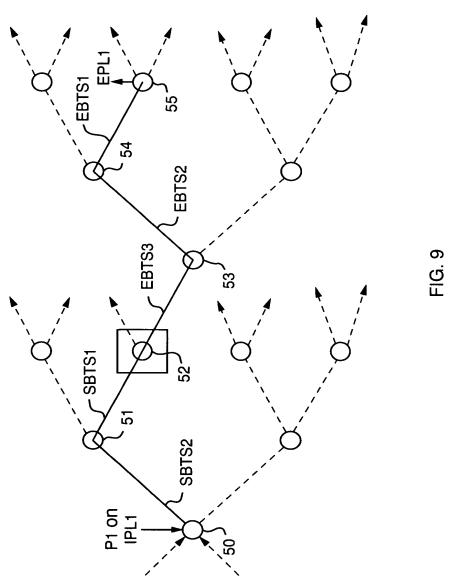
Application No. 09/866,335

Title: System and Method for Implement (1997)

Application No. 09/866,335

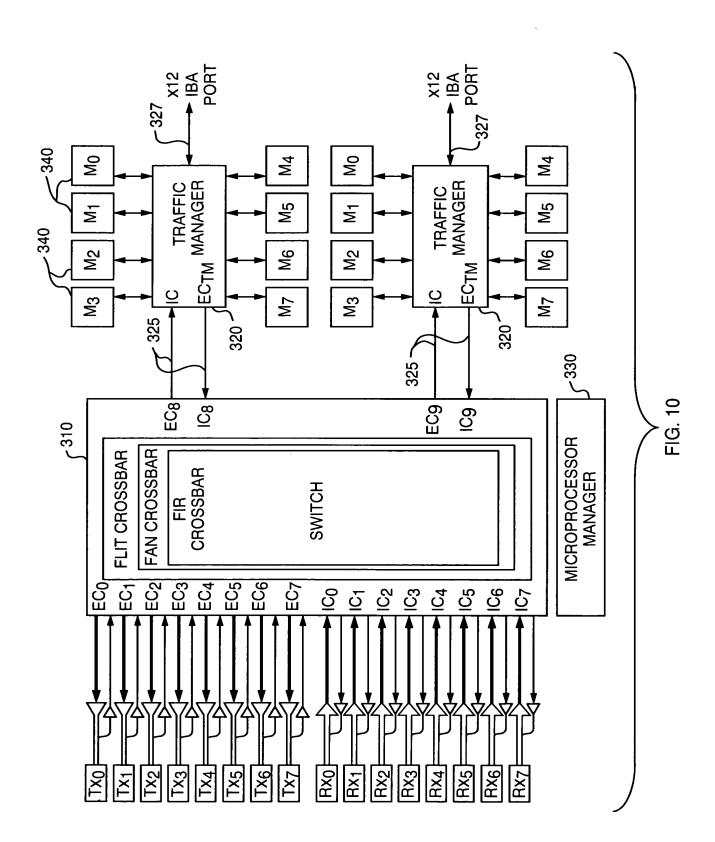
Title: System and Method for Implement (1997)

Application No. 09/866,335



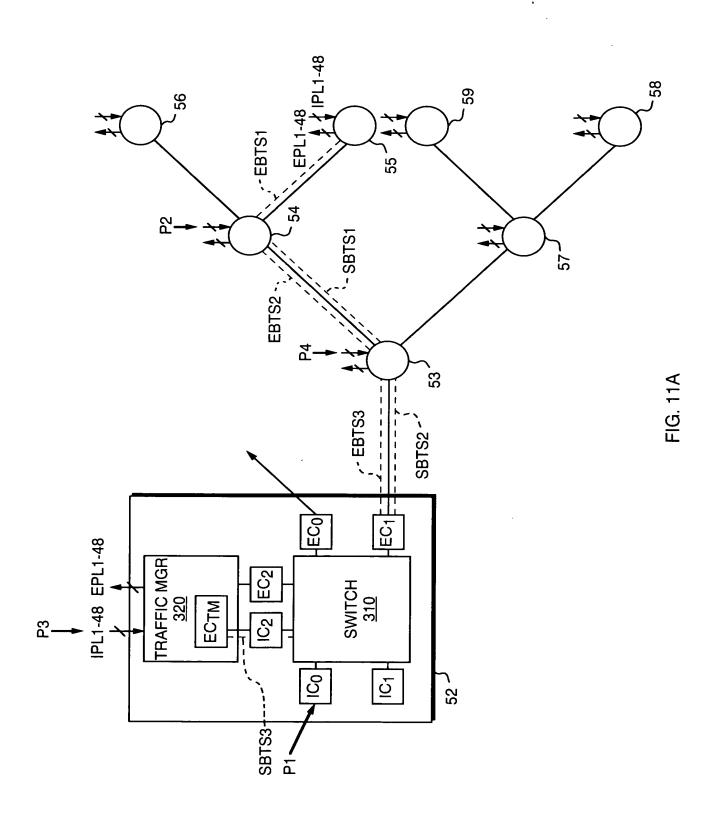
Title: System and Method for Implem inventor: Philip P. Carvey





Application No. 09/866,335

Title: System and Method for Implem Inventor: Philip P. Carvey



2 HOPS

EPL1 EPL1 EPL1

EPL48

EPL1

71 HOP

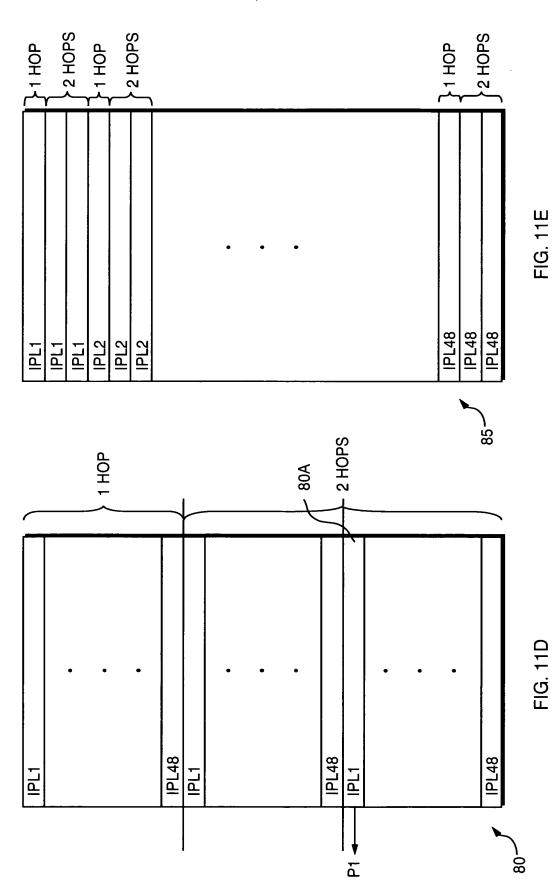
plication No. 09/866,335 le: System and Method for Implement entor: Philip P. Carvey Application No. entor: 15/21 3 HOPS >2 HOPS >3 HOPS FIG. 11C EPL48 EPL48 EPL48 EPL48 EPL48 EPL48 EPL48 EPL1 EPL1 EPL1 751 2 HOPS 3 HOPS 70A FIG. 11B EPL48 EPL48 EPL48 EPL48 EPL48 EPL48 EPL1 EPL1 EPL1 EPL1 EPL1 EPL1 707 딥

Application No. 09/866,335

itle: System and Method for Impleme ...

nventor: Philip P. Carvey

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Application No.

Application No. 09/866,335

Sitle: System and Method for Implement of the Price of

nventor:

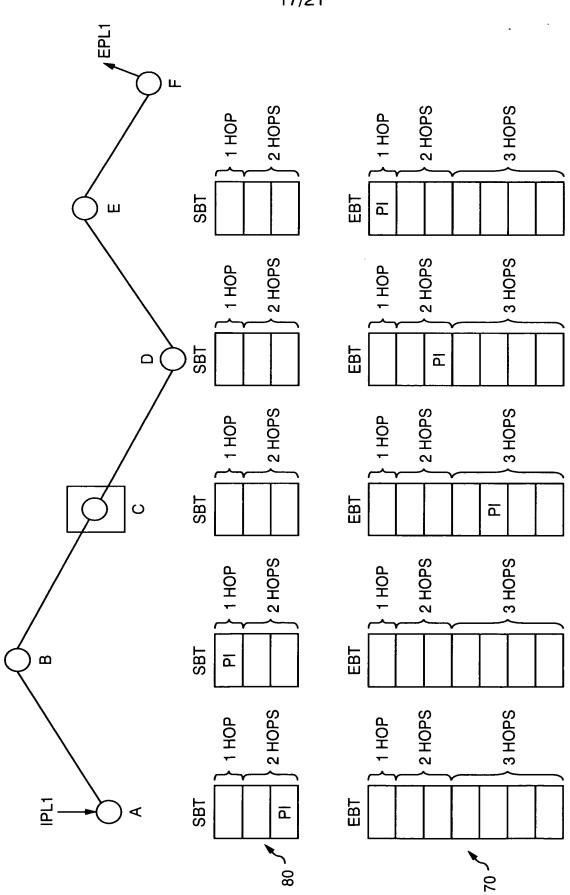
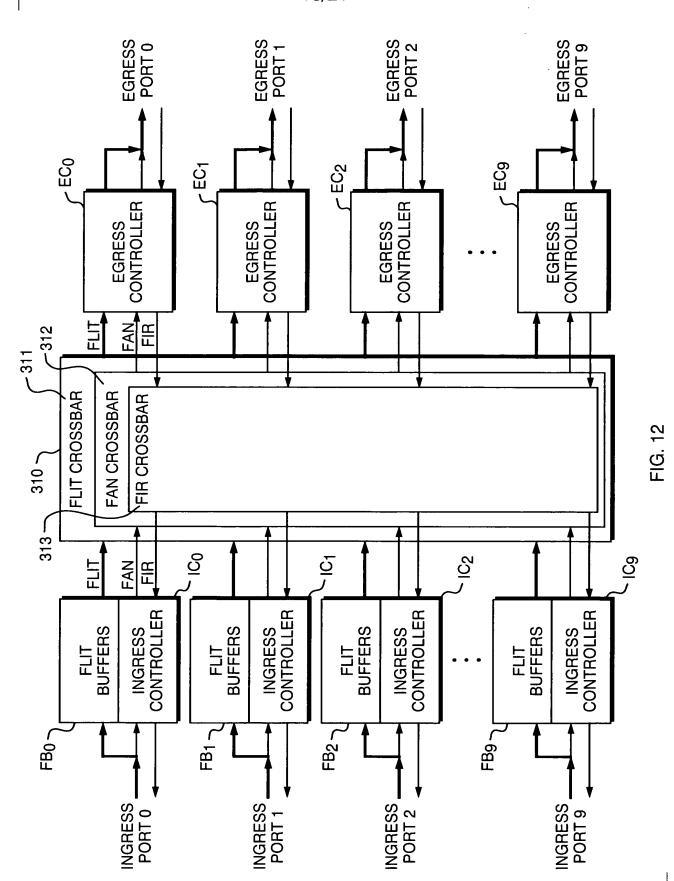


FIG. 11F

oplication No. 09/866,335 tle: System and Method for Implemen Application No.

Philip P. Carvey



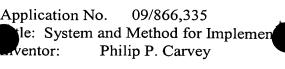
Application No. 09/866,335

Itle: System and Method for Implementation Philip P. Carvey



CONTROL STRUCTURE	SIZE (IN BITS)	DESCRIPTION
IngressPacketState	1280x35=44,800	EACH IngressPacketState STRUCTURE MANAGES THE STORAGE OF A PARTIALLY RECEIVED PACKET ON ONE OF THE INGRESS PORTS.
EgressLaneState	(128x30=3,480)	EACH EgressLaneState STRUCTURE SUPPLIES INFORMATION USED TO PROCESS RECEIVED CREDITS.
AvailableEgressLane	(128x1)	EACH FLAG INDICATES THAT A PARTICULAR LANE IS AVAILABLE OR IN USE.
FanState	(512x44=22,528)	EACH FanState STRUCTURE HOLDS ONE FAN WAITING TO BE CONVERTED INTO A FIR AND POINTERS WHICH ALLOW CREATING A LINKED LIST OF PACKETS WAITING ON A PARTICULAR CHANNEL AND A LINKED LIST OF FANS COMPRISING A PARTICULAR PACKET.
AvailableFanState	(512x1)	EACH FLAG INDICATES THAT A PARTICULAR LOCAL FANSTATE STRUCTURE IS AVAILABLE OR IN USE.
WaitingForLanes	(2928x1)	EACH FLAG INDICATES THAT A PARTICULAR TUNNEL SEGMENT HAS A PACKET READY TO BE ASSIGNED TO A LANE AS SOON AS ONE BECOMES AVAILABLE.
WaitingForFSM	(2928x1)	EACH FLAG INDICATES THAT A PARTICULAR CHANNEL HAS A FAN READY TO BE CONVERTED INTO A FIR AS SOON AS THE EgressController HAS BANDWIDTH AVAILABLE TO PERFORM THE CONVERSION.
WaitingForFirFifo	(2304x1)	EACH FLAG INDICATES THAT A PARTICULAR LANE HAS A FAN READY TO CONVERT INTO A FIR AS SOON AS ROOM IN THE FIR FIFO BECOMES NON-FULL.
SegmentPointer	(2938x13=38,194)	EACH SegmentPointer POINTS TO A QUEUE OF PACKETS WAITING ON A TUNNEL SEGMENT.

Application No.



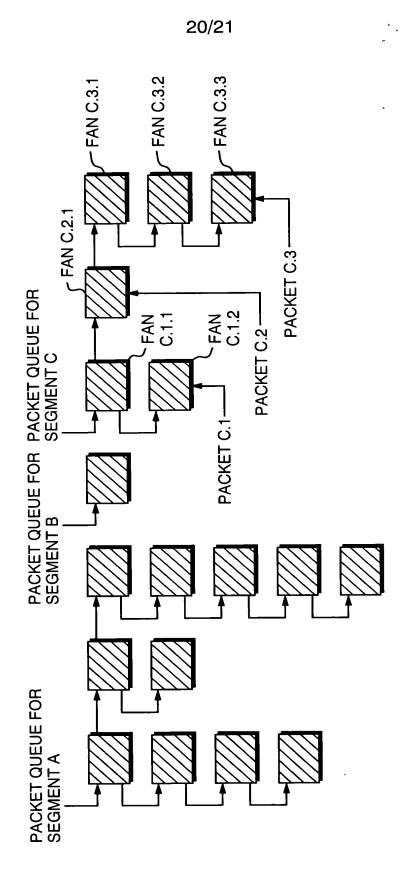


FIG. 14

Title: System and Method for Implem Philip P. Carvey inventor: ingressController 6 IngressController 5 IngressController 3 FIRs TO FIRS TO IngressController 4 IngressController.2 ingresscontroller 0 IngressController 9 FIRS TO IngressController 7 IngressController 8 21/21 IngressController FIRS TO FIRFIF00 FIRFIF08 FIRFIF04 FIRFIF06 FIRFIF02 7400 FIRFIF09 FIRFIF05 **FIRFIF03** FIRFIF01 FIRFIF07 420 LANE STATE MEMORY GENERATOR **PROCESSOR** 123x456 450 FIRFIFO 500 490 STATE MEMORY 떒 WAITING 123x456 470 480 **PROCESSOR PROCESSOR** FLIT STATE MEMORY 7 440 123x456 FIG. 15 CREDIT FAN 410 460 SELECTOR STATE MEMORY 123x456 **PACKET** QUEUE MEMORY SEGMENT 2938x13 FAN 430 / FANFIF09 FANFIF03 FANFIF07 FANFIF05 FANFIF01 CREDIT FANFIF02 FANFIF04 FANFIF08 FANFIF06 FANFIF00 IngressController 5
FANS FROM ______ ngressController9 IngressController 6 IngressController 3 IngressController 2 IngressController 0 IngressController 8 IngressController 7 IngressController IngressController FANS FROM -FANS FROM FANS FROM FANS FROM FANS FROM FANS FROM **FANS FROM** FANS FROM FANS FROM

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Application No.